## DOD DESKBOOK 5000.2-R

## <u>APPENDIX 6</u> <u>TECHNOLOGY READINESS LEVELS AND THEIR DEFINITIONS</u>

## AP1.1. TECHNOLOGY READINESS LEVELS

The following matrix lists the various technology readiness levels and descriptions from a systems approach for both HARDWARE and SOFTWARE. DoD Components may provide additional clarifications for Software. Supplemental definitions follow the table.

| Technology Readiness Level                                                               | Description                                                                                                                                                                                                                                                                           |
|------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1. Basic principles observed and reported.                                               | Lowest level of technology readiness. Scientific<br>research begins to be translated into applied<br>research and development. Examples might<br>include paper studies of a technology's basic<br>properties.                                                                         |
| 2. Technology concept and/or application formulated.                                     | Invention begins. Once basic principles are<br>observed, practical applications can be invented.<br>Applications are speculative and there may be<br>no proof or detailed analysis to support the<br>assumptions. Examples are limited to analytic<br>studies.                        |
| 3. Analytical and experimental critical function and/or characteristic proof of concept. | Active research and development is initiated.<br>This includes analytical studies and laboratory<br>studies to physically validate analytical<br>predictions of separate elements of the<br>technology. Examples include components that<br>are not yet integrated or representative. |
| 4. Component and/or breadboard validation in laboratory environment.                     | Basic technological components are integrated<br>to establish that they will work together. This is<br>relatively "low fidelity" compared to the<br>eventual system. Examples include integration<br>of "ad hoc" hardware in the laboratory.                                          |
| 5. Component and/or breadboard validation in                                             | Fidelity of breadboard technology increases                                                                                                                                                                                                                                           |

| relevant environment.                                                           | significantly. The basic technological<br>components are integrated with reasonably<br>realistic supporting elements so it can be tested<br>in a simulated environment. Examples include<br>"high fidelity" laboratory integration of<br>components.                                                                               |
|---------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 6. System/subsystem model or prototype demonstration in a relevant environment. | Representative model or prototype system,<br>which is well beyond that of TRL 5, is tested in<br>a relevant environment. Represents a major step<br>up in a technology's demonstrated readiness.<br>Examples include testing a prototype in a high-<br>fidelity laboratory environment or in simulated<br>operational environment. |
| 7. System prototype demonstration in an operational environment.                | Prototype near, or at, planned operational<br>system. Represents a major step up from TRL 6,<br>requiring demonstration of an actual system<br>prototype in an operational environment such as<br>an aircraft, vehicle, or space. Examples include<br>testing the prototype in a test bed aircraft.                                |
| 8. Actual system completed and qualified through test and demonstration.        | Technology has been proven to work in its final<br>form and under expected conditions. In almost<br>all cases, this TRL represents the end of true<br>system development. Examples include<br>developmental test and evaluation of the system<br>in its intended weapon system to determine if it<br>meets design specifications.  |
| 9. Actual system proven through successful mission operations.                  | Actual application of the technology in its final<br>form and under mission conditions, such as<br>those encountered in operational test and<br>evaluation. Examples include using the system<br>under operational mission conditions.                                                                                             |

## **DEFINITIONS**:

BREADBOARD: Integrated components that provide a representation of a system/subsystem and which can be used to determine concept feasibility and to develop technical data. Typically configured for laboratory use to demonstrate the technical principles of immediate interest. May resemble final system/subsystem in function only.

"HIGH FIDELITY": Addresses form, fit and function. High-fidelity laboratory environment would involve testing with equipment that can simulate and validate all system specifications within a laboratory setting.

"LOW FIDELITY": A representative of the component or system that has limited ability to provide anything but first order information about the end product. Low-fidelity assessments are used to provide trend analysis.

MODEL: A functional form of a system, generally reduced in scale, near or at operational specification. Models will be sufficiently hardened to allow demonstration of the technical and operational capabilities required of the final system.

OPERATIONAL ENVIRONMENT: Environment that addresses all of the operational requirements and specifications required of the final system to include platform/packaging.

PROTOTYPE: A physical or virtual model used to evaluate the technical or manufacturing feasibility or military utility of a particular technology or process, concept, end item or system.

RELEVANT ENVIRONMENT: Testing environment that simulates the key aspects of the operational environment.

SIMULATED OPERATIONAL ENVIRONMENTAL: Either 1) a real environment that can simulate all of the operational requirements and specifications required of the final system, or 2) a simulated environment that allows for testing of a virtual prototype; used in either case to determine whether a developmental system meets the operational requirements and specifications of the final system.